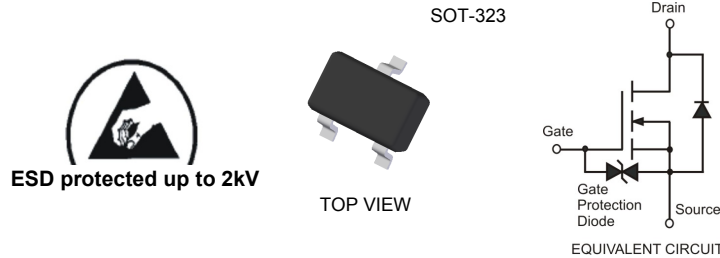


Features

- Low On-Resistance
- Very Low Gate Threshold Voltage (1.0V max)
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **ESD Protected Up To 2kV**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 standards for High Reliability**

Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish – Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.006 grams (approximate)



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	50	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 1)	I _D	Continuous	300
		Pulsed (Note 3)	800

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P _D	250	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	50	—	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	60	nA	@ T _C = 25°C V _{DS} = 50V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	1	μA	V _{GS} = ±12V, V _{DS} = 0V
				500	nA	V _{GS} = ±10V, V _{DS} = 0V
				50	nA	V _{GS} = ±5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	0.49	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	—	3.0	Ω	V _{GS} = 1.8V, I _D = 50mA
				2.5	Ω	V _{GS} = 2.5V, I _D = 50mA
				2.0	Ω	V _{GS} = 5.0V, I _D = 50mA
On-State Drain Current	I _{D(on)}	0.5	1.4	—	A	V _{GS} = 10V, V _{DS} = 7.5V
Forward Transconductance	Y _{fs}	200	—	—	mS	V _{DS} = 10V, I _D = 0.2A
Source-Drain Diode Forward Voltage	V _{SD}	0.5	—	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iSS}	—	—	50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C _{rSS}	—	—	5.0	pF	

- Notes:
1. Device mounted on FR-4 PCB.
 2. No purposefully added lead.
 3. Pulse width ≤10μS, Duty Cycle ≤1%.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 5. Short duration pulse test used to minimize self-heating effect.

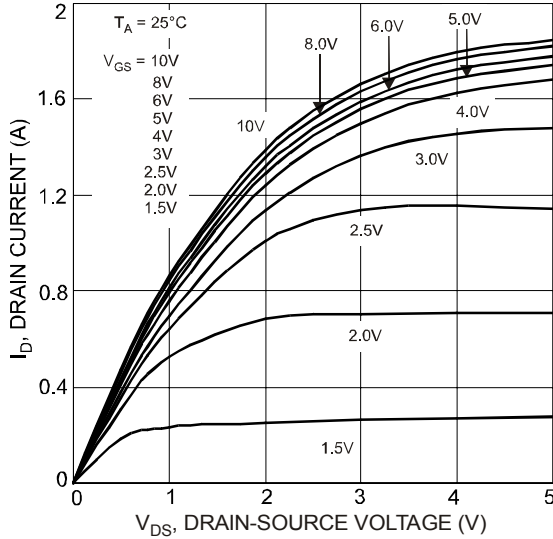


Fig. 1 Typical Output Characteristics

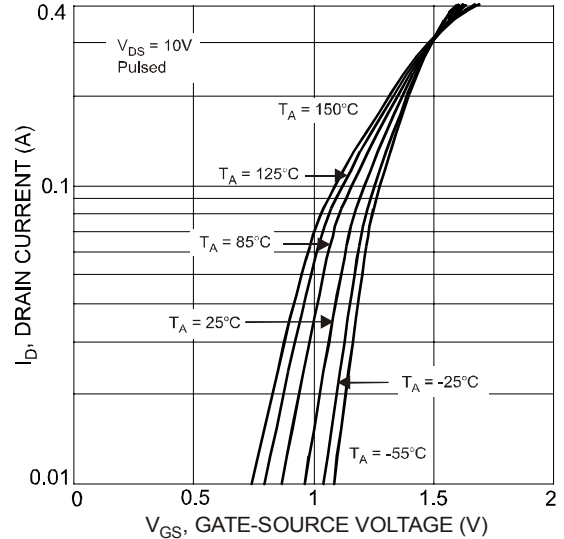


Fig. 2 Typical Transfer Characteristics

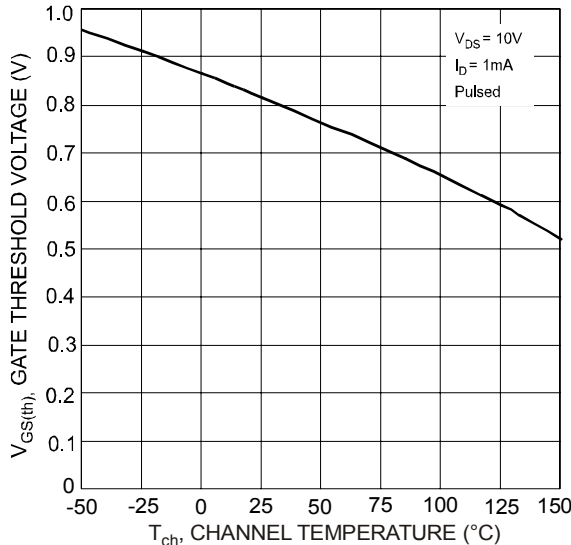


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

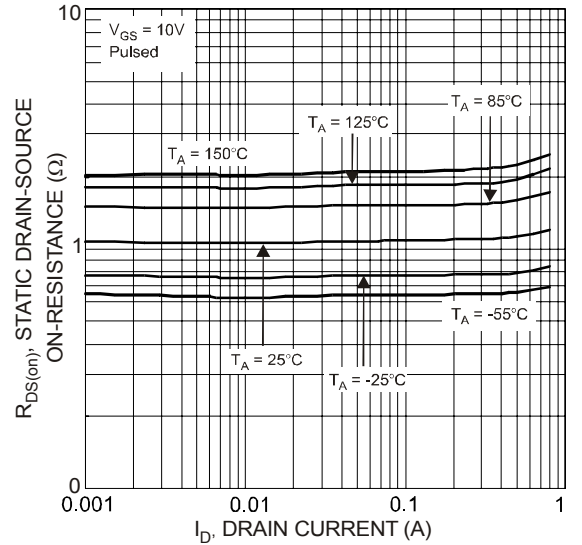


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

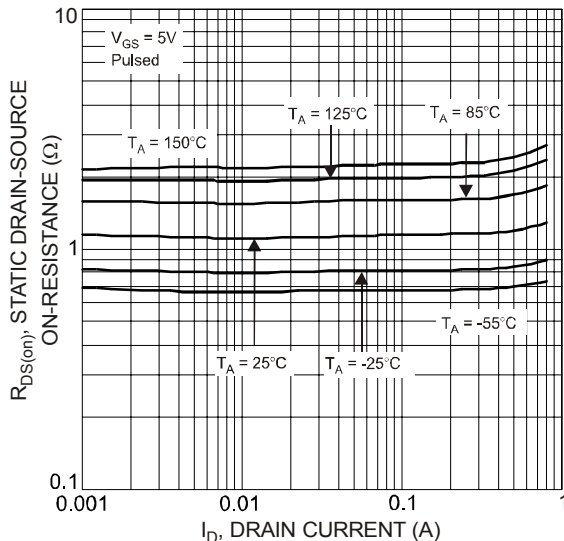


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

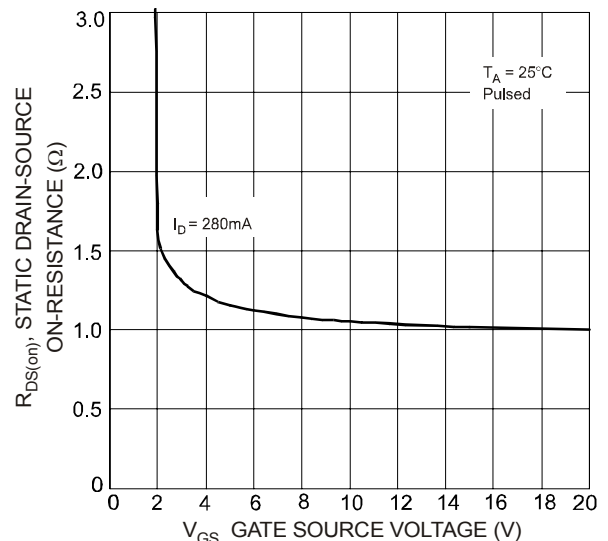


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage

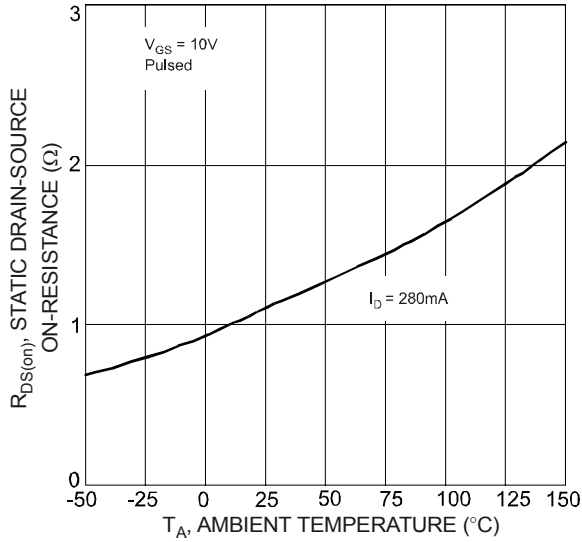


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

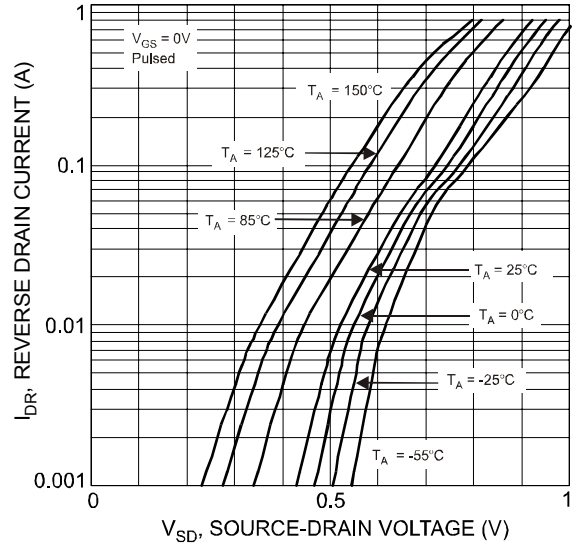


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

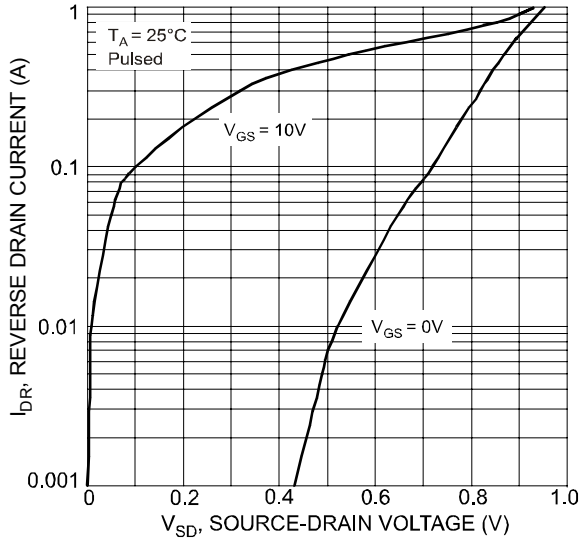


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

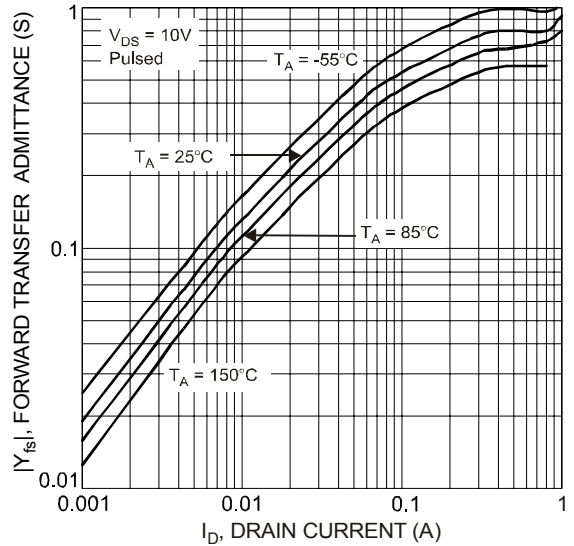


Fig. 10 Forward Transfer Admittance vs. Drain Current

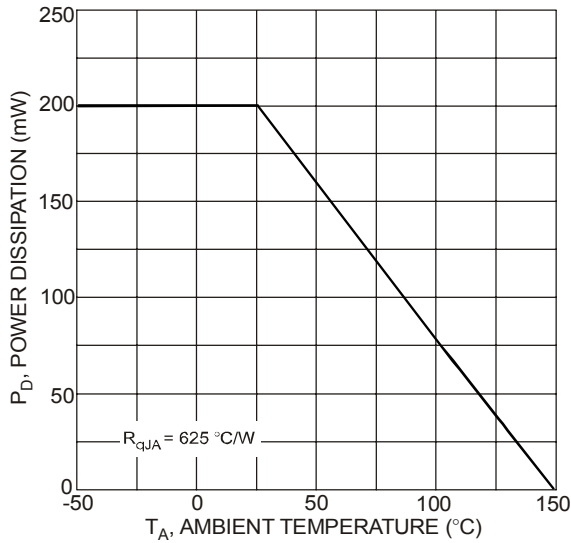


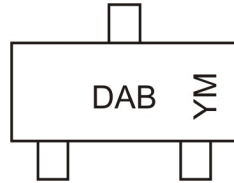
Fig. 11 Derating Curve - Total

Ordering Information (Note 6)

Part Number DMN5L06WK-7	Case SOT-323	Packaging 3000/Tape & Reel
-----------------------------------	------------------------	--------------------------------------

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



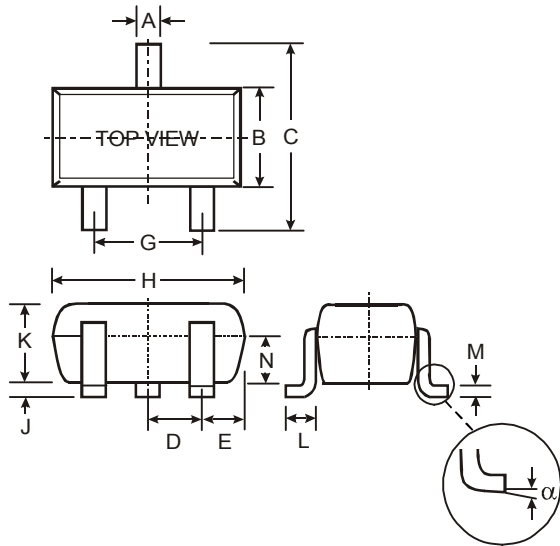
DAB = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

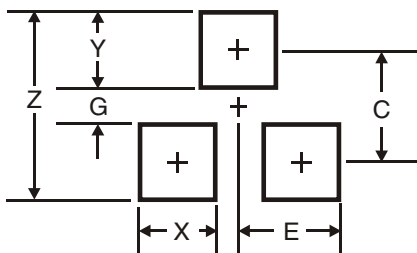
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-323		
Dim	Min	Max
A	0.25	0.25
B	1.15	1.35
C	2.00	2.00
D	0.65 Nominal	
E	0.30	0.40
G	1.20	1.40
H	1.80	2.20
J	0.0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.18
N	—	—
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
G	1.0
X	0.7
Y	0.9
C	1.9
E	0.65

IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.